

MIPSFpga One-Day Workshops

02 May - 04 May 2016

Bay Area, Portland

Get Hands-on With A Non-obfuscated Fully-verified Industry-proven MIPS Core for Teaching and Projects!

- Add your own peripherals
- Add your own coprocessor
- Observe the pipeline behaviour
- Experiment with performance
- Same Verilog code as in Samsung Artik 1

http://bit.ly/mipsfpga_2016_usa

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